

J1000 U.S. PTO
09/94078
08/27/01

	Subclass	Class	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

KS W.D.I.P.E. SCANNED QO3 Q.A. AA	PATENT DATE
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APPLICATION NO. 09/940278	CONT/PRIOR	CLASS 361 EY	SUBCLASS 260 29	ART UNIT 2841 2821	EXAMINER PATEF Abes PATER
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Stephen Ohr

29 896 3729

Layered circuit boards and methods of production thereof

TITLE

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) _____ (Date)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date)			Amount Due	Date Paid
ISSUE BATCH NUMBER					

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